



- NOTE:**
1. Recommended currents are : $I_{dreq1}=0.25A$, $I_{dreq2}=1.05A$ & $I_{dreq3}=1.05A$ for the first, second & third stage respectively.
 2. Gate $V_{gs1,2,3}$ bias of $-0.87V$ are for reference only. $V_{gs1,2,3}$ could be adjusted to vary currents going thru the module.
 3. Do **NOT** apply V_{ds1} & V_{ds2} & V_{ds3} without proper negative voltages.

PIN NO.	FUNCTION	BIAS
1	V_{gs1}, V_{ds2}	-0.87V
2	NC	-
3	V_{gs3}	-0.87V
4	V_{ds3}	+8V
5	V_{ds2}	+8V
6	V_{ds1}	+8V

STANDARD TOLERANCES
 .X ±0.2
 .XX ±0.01
 .XXX ±0.005

*STANDARD TOLERANCES APPLY ONLY TO DIMENSIONS IN INCHES



NOTES:
 1. UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE NOMINAL.
 2. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE AT ANY TIME.
 3. DIMENSIONS ARE IN INCHES [mm].

TITLE		DWG NO		CAGE CODE	
		FMAM5009		3FKR5	
CAD FILE	05/18/18	SHEET	1 OF 1	SCALE	N/A
		SIZE	A		7361