

DETAIL A

D-SUB 9-PIN, MALE	
PIN #	DESCRIPTION
1,2,3	VDD 28 VDC
4,5,6	GND
7	ENABLE Amplifier Enable: TTL Logic High (3.3V) (Internally Pulled-Low)
8	CURRENT MONITOR Analog voltage relative to IDD @100mV per Ampere
9	TEMP MONITOR Analog voltage relative to Module's Temperature @10 mV/°C

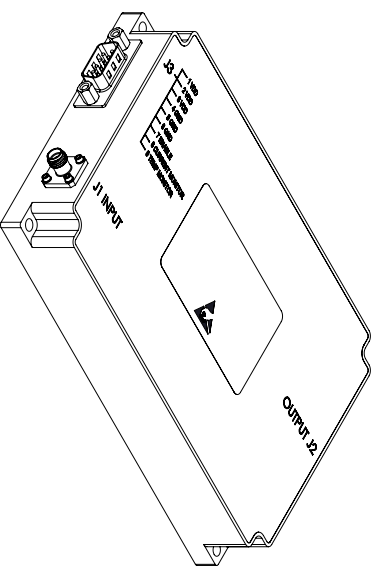
LABEL



STANDARD TOLERANCES  
 .X ±0.2  
 .XX ±0.01  
 .XXX ±0.005

\*STANDARD TOLERANCES APPLY ONLY TO DIMENSIONS IN INCHES

- NOTES:
- ADEQUATE HEATSINK REQUIRED.



TITLE

NOTES:  
 1. UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE NOMINAL.  
 2. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE AT ANY TIME.  
 3. DIMENSIONS ARE IN INCHES [mm].

DWG NO	FMAM5066	CAGE CODE	3FKR5
CAD FILE	07/30/19	SHEET	1 OF 1
SCALE	N/A	SIZE	A
			7361