

HYBRID, D-SUB 7-PIN MALE		SPECIFICATIONS	
PIN #	DESCRIPTION		
A1	VDD	24 VDC	
A2	GND	Ground	
1	ENABLE	Amplifier Enable: TTL Logic High (3.3V) (Internally Pulled-Low)	
2	TEMP SENSE	Analog voltage relative to Module's Temperature @ 10 mV/°C	
3	CURRENT SENSE	Analog voltage relative to IDD@ 100mV per Ampere	
4	NC	No electrical connection	
5	GND	Ground	

DETAIL A

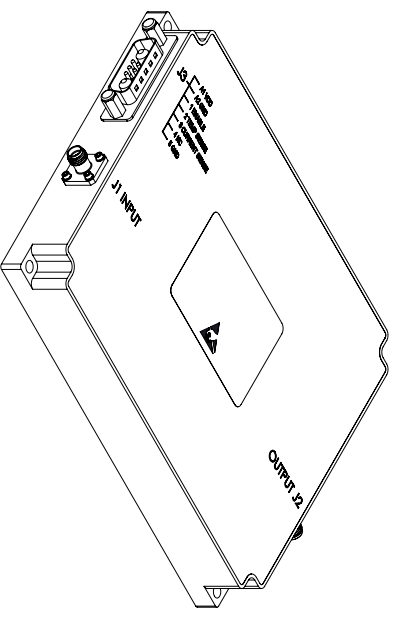
LABEL



STANDARD TOLERANCES
 .X ±0.2
 .XX ±0.01
 .XXX ±0.005

*STANDARD TOLERANCES APPLY ONLY TO DIMENSIONS IN INCHES

- NOTES:
- ADEQUATE HEATSINK REQUIRED.



TITLE

NOTES:
 1. UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE NOMINAL.
 2. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE AT ANY TIME.
 3. DIMENSIONS ARE IN INCHES [mm].

DWG NO		CAGE CODE	
FMAM5069		3FKR5	
CAD FILE	07/30/19	SHEET	1 OF 1
SCALE N/A		SIZE A	
		7361	